

Fig. 1

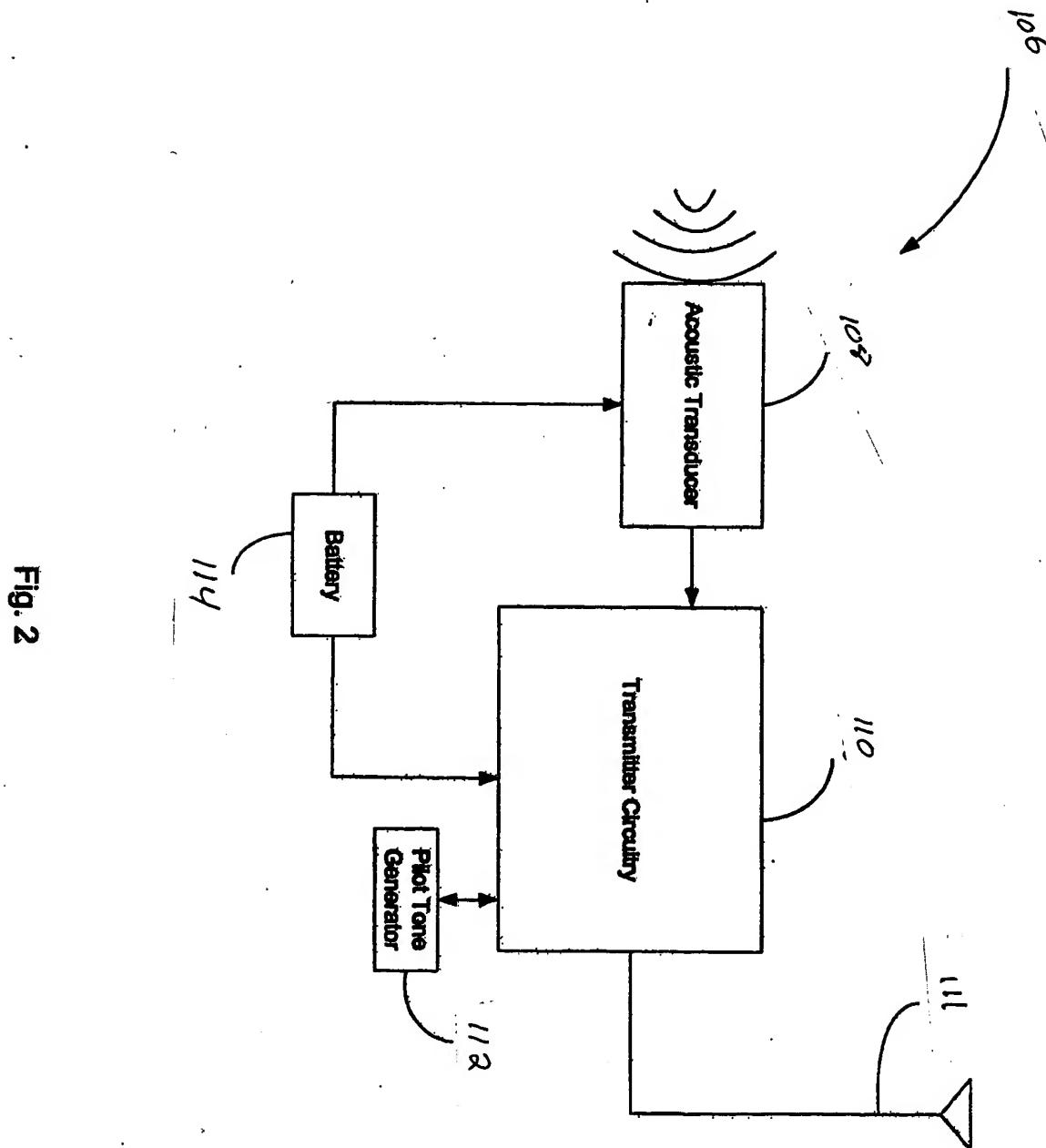


Fig. 2

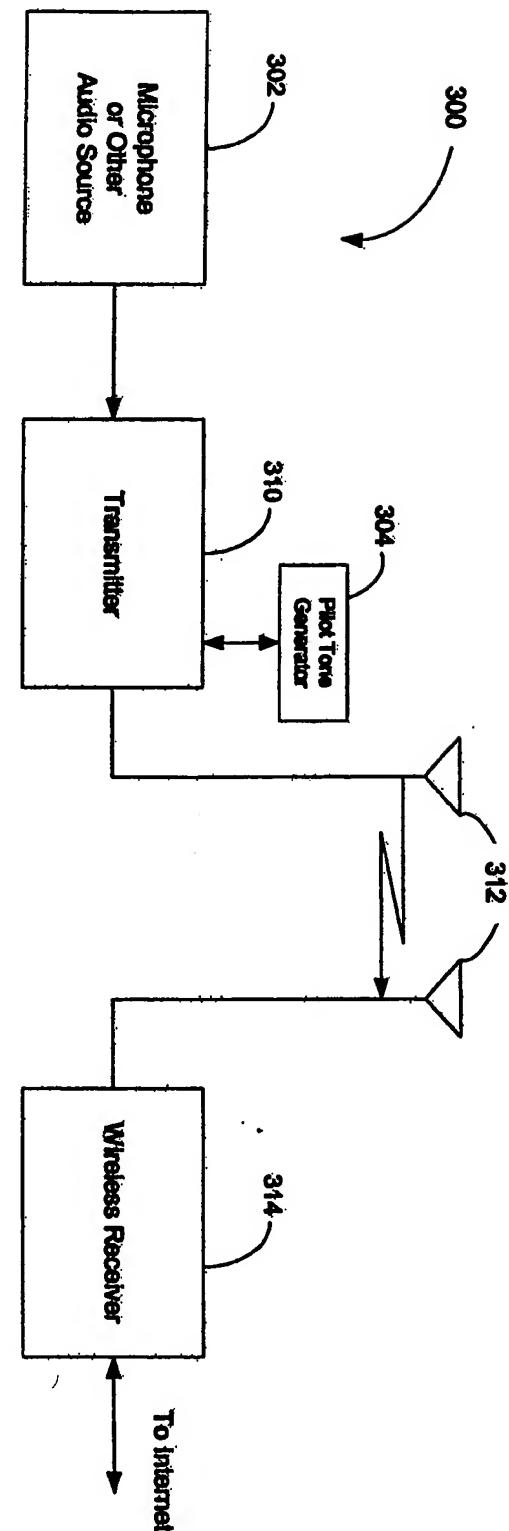
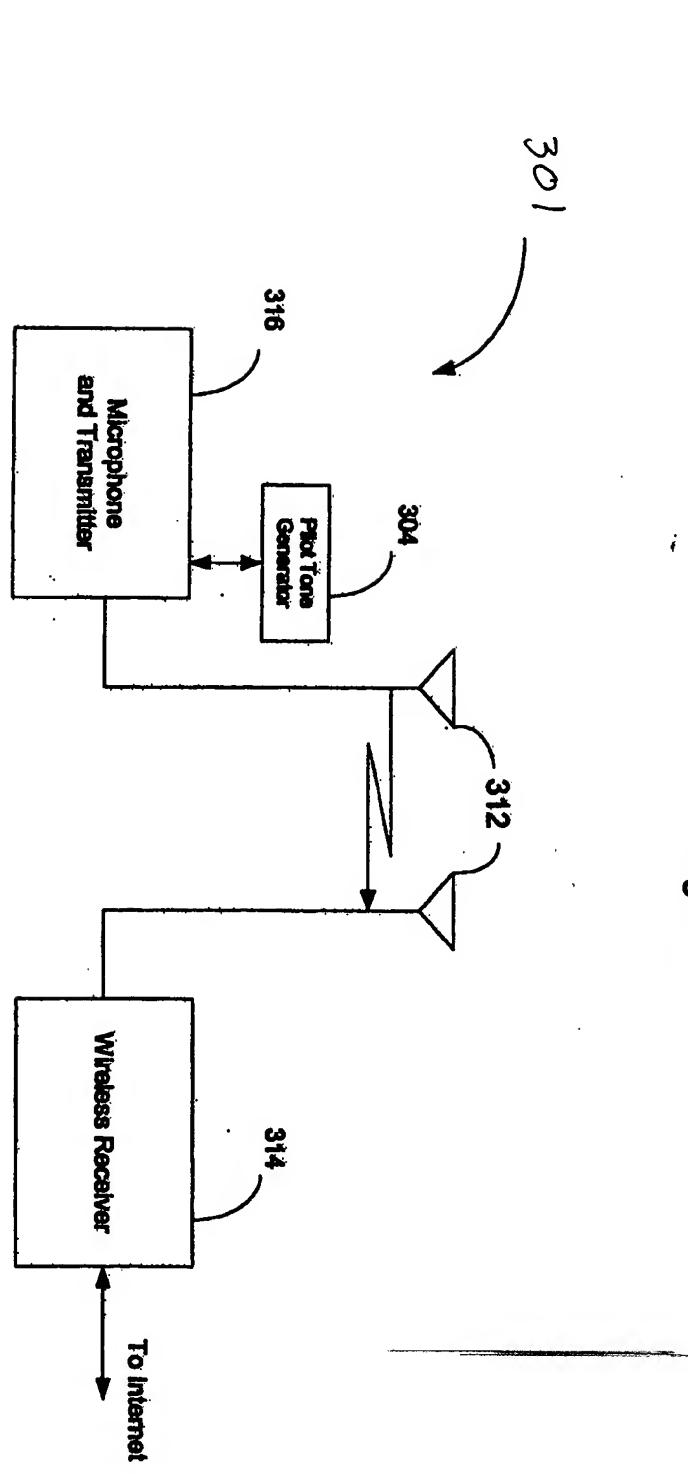


Fig. 3A

Fig. 3B

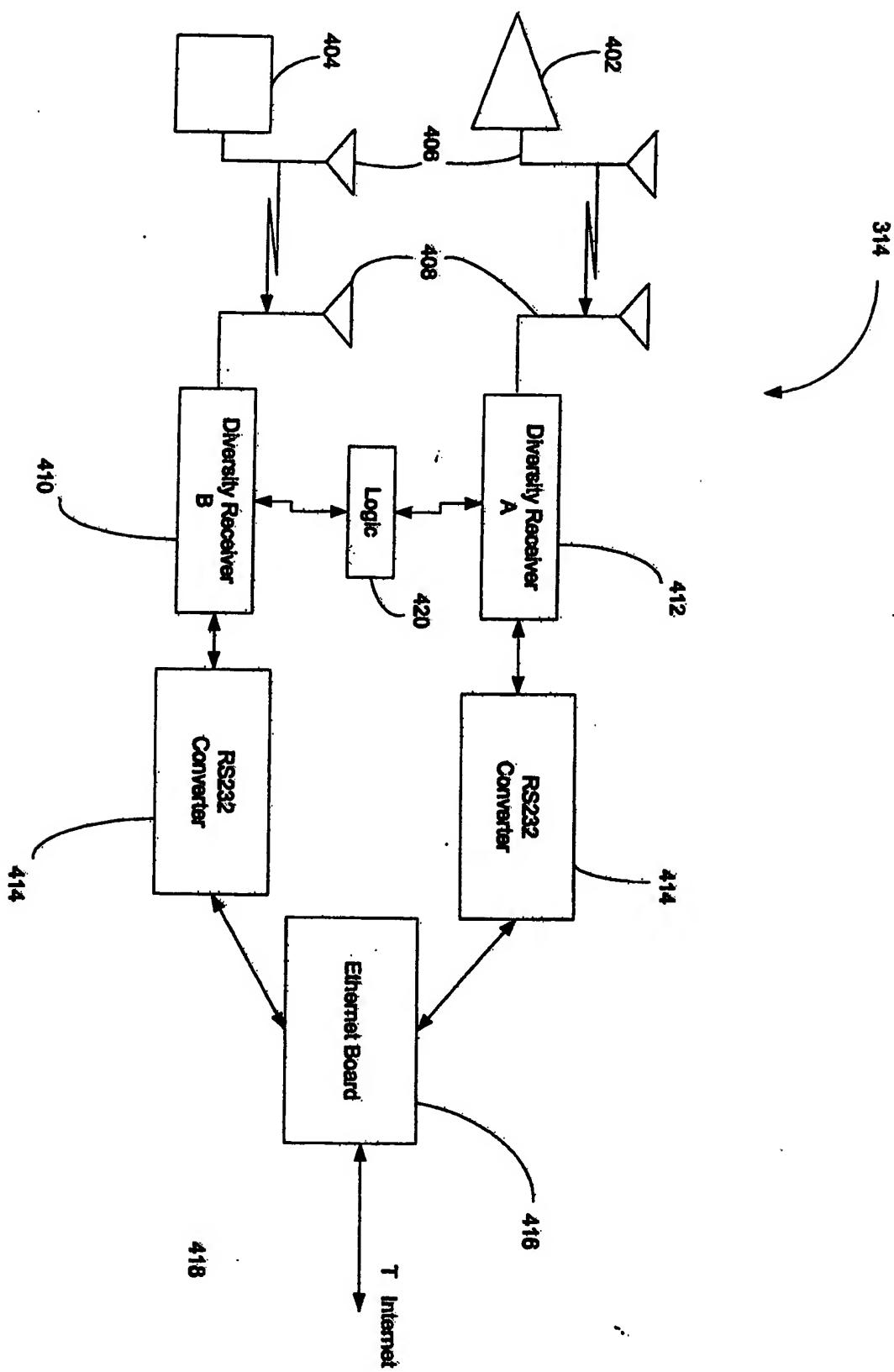


Fig. 4

500

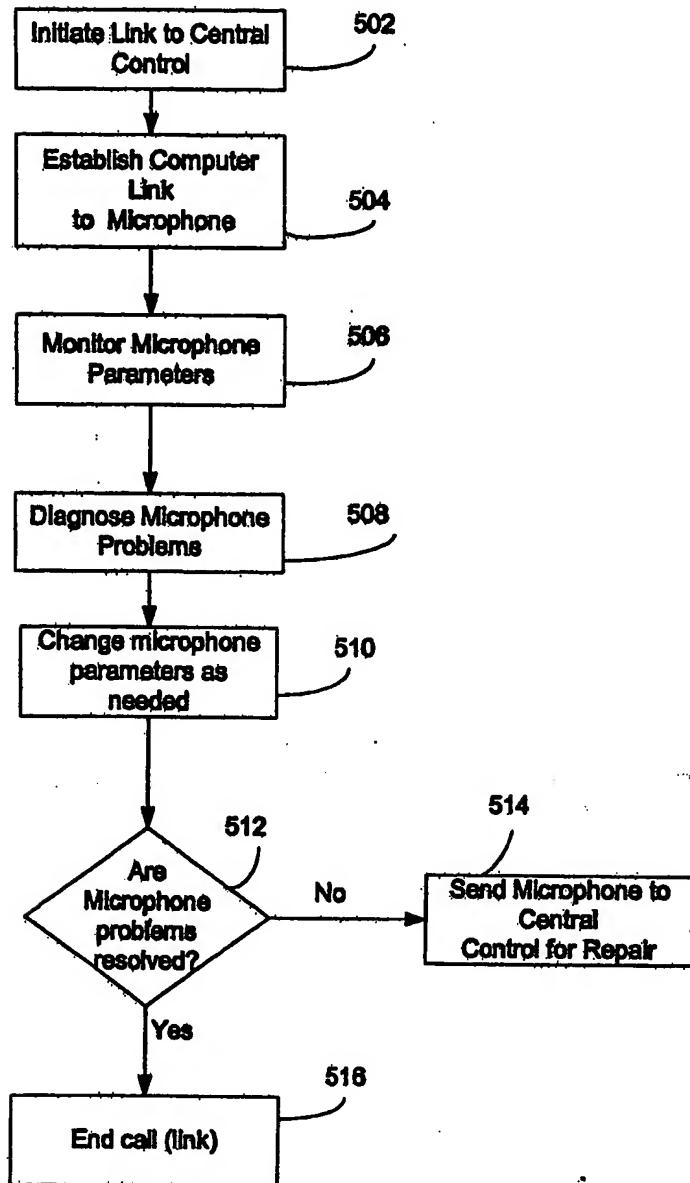


FIG. 5

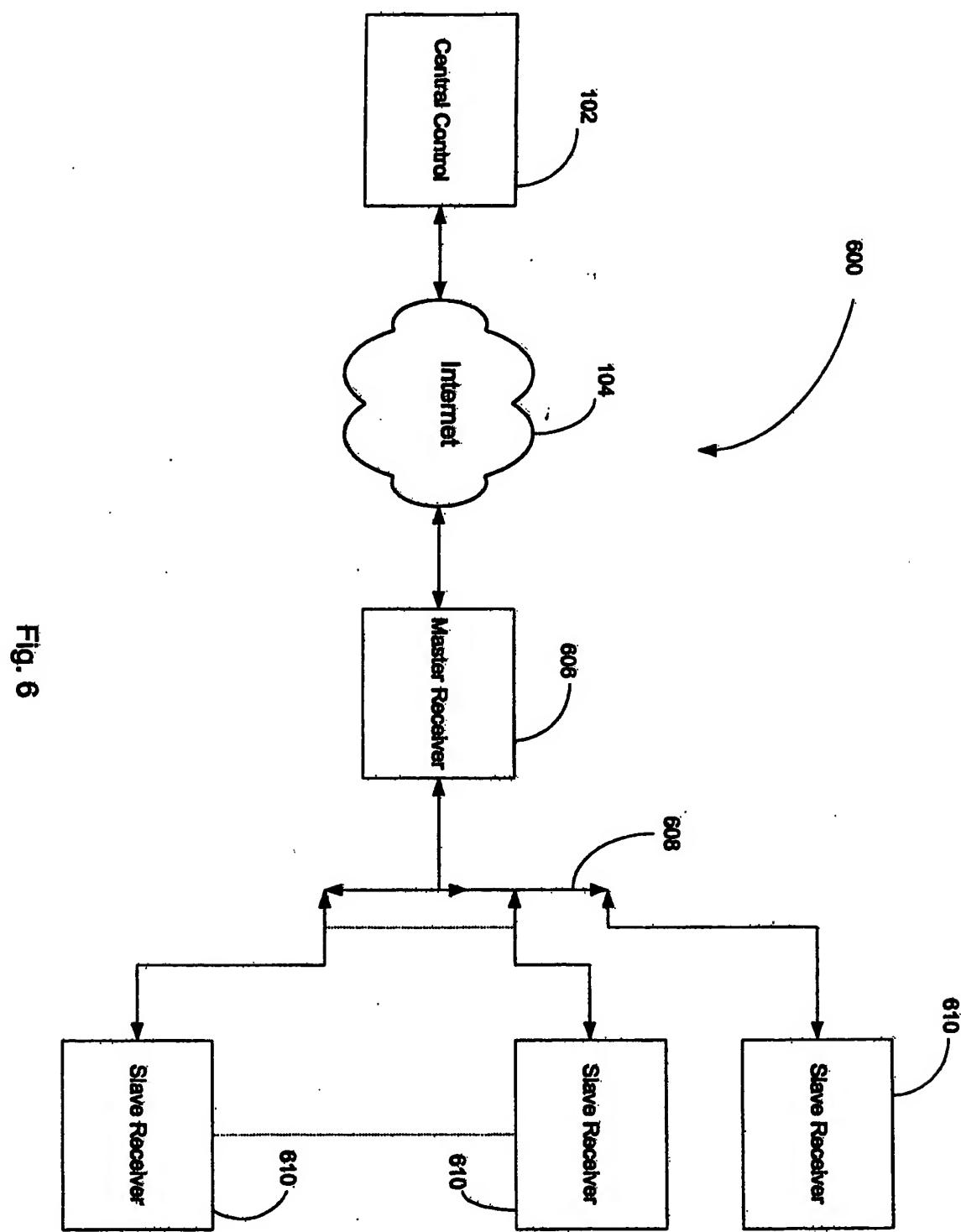
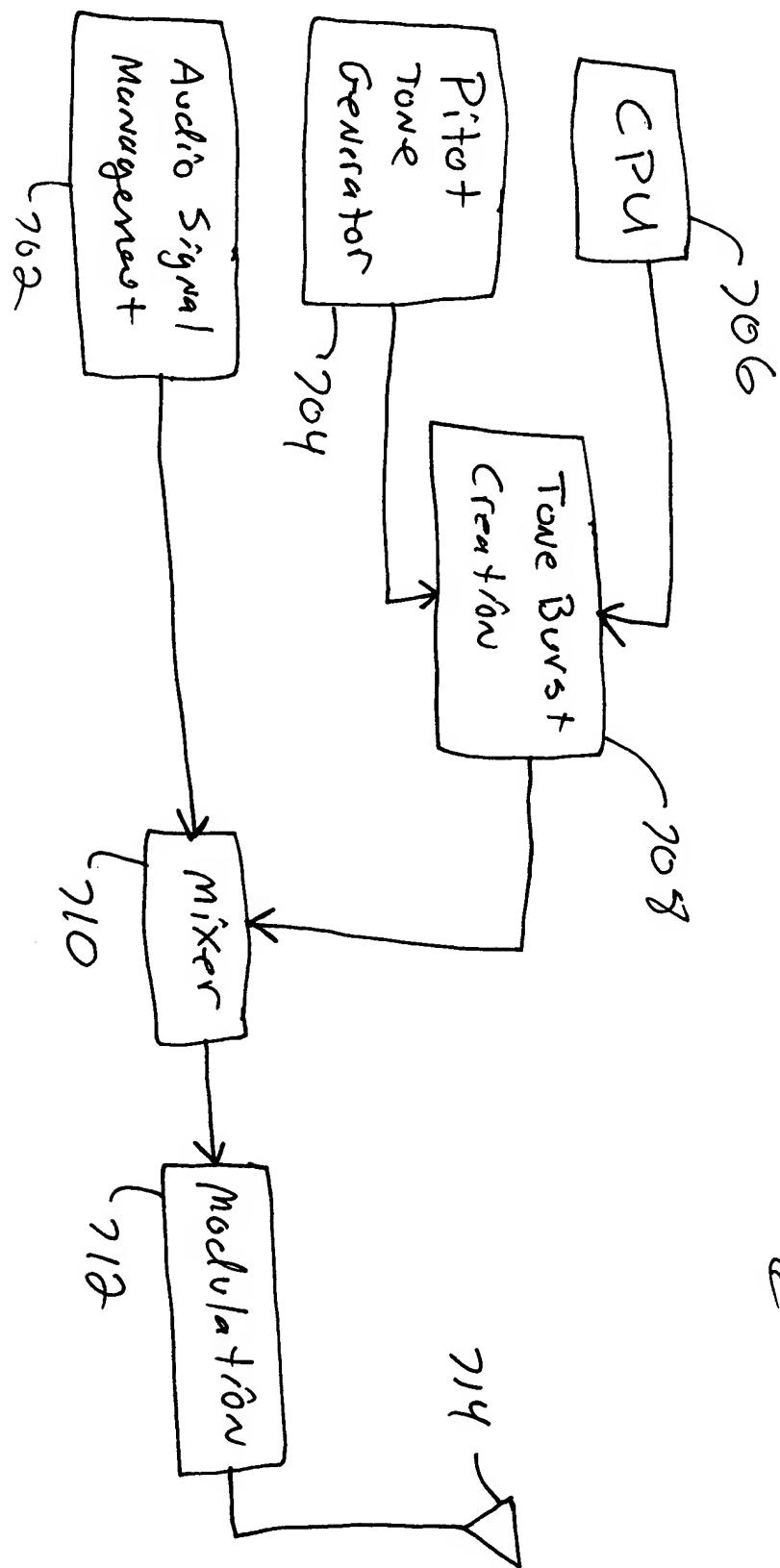


Fig. 6

Fig. 7



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Fig. 8

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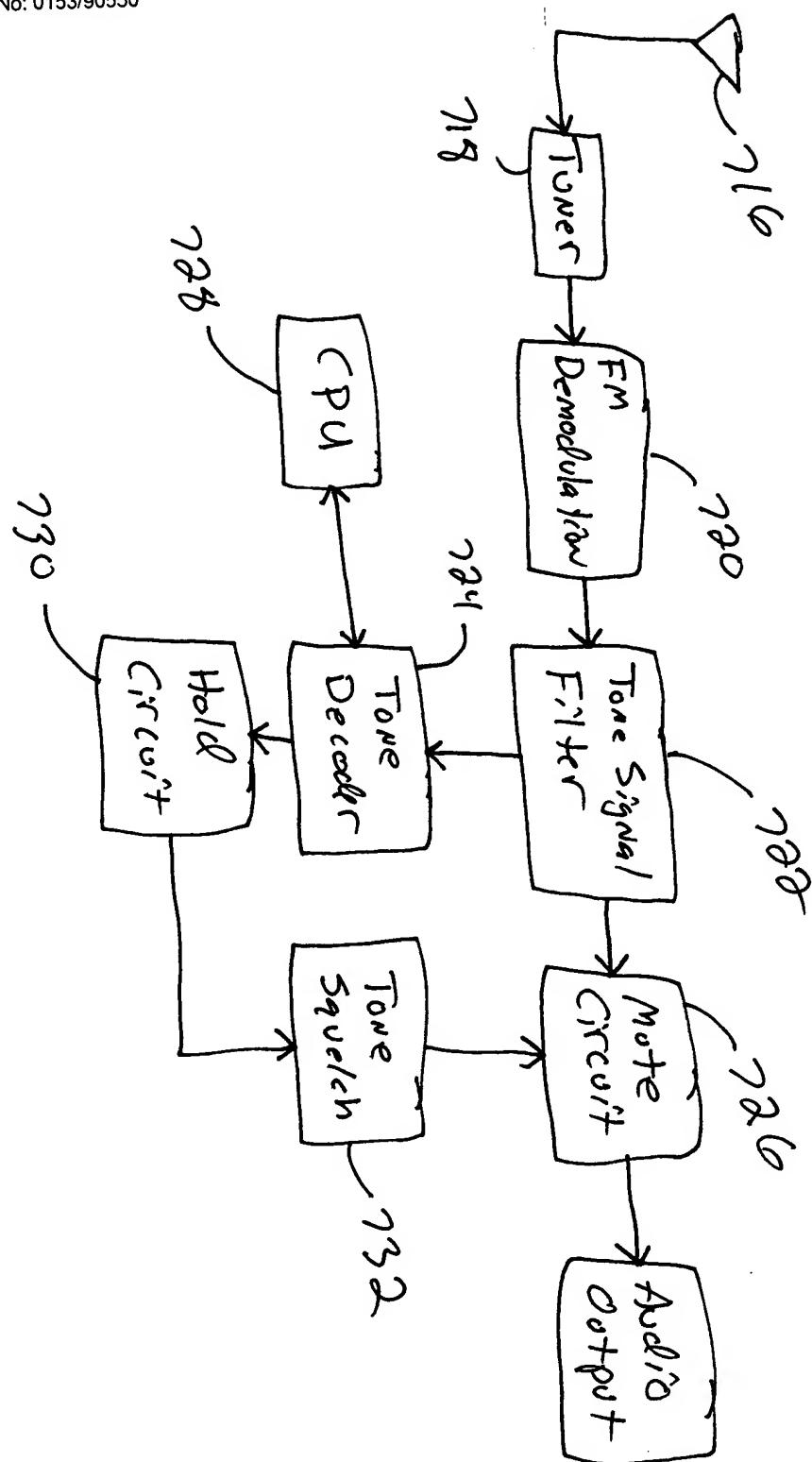


Fig. 9A



Fig. 9B

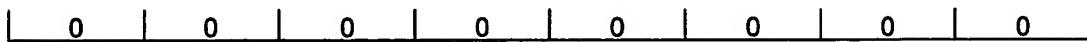
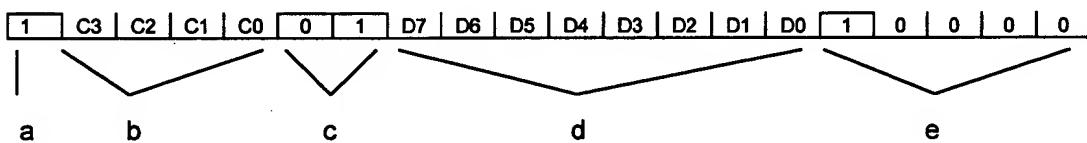


Fig. 9C



- a. Start Bit (1bit)
- b. Index (4bit)
- c. Separator (2bit)
- d. Data (8bit)
- e. Stop Sequence (5bit)